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	INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	09/768,504
	(use as many sheets as necessary)		Filing Date	01/25/01
			First Named Inventor	Priegnitz
			Group Art Unit	TBD
			Examiner Name	TBD
			Attorney Docket No.	

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CA	IONEL DAN JITARU, High-Efficiency Rectification Techniques for Low Output Voltage and High Output Voltage Applications, Fourteenth Annual Applied Power Electronics Conference, Dallas, Texas, March 1999, APEC'99 Seminar 3	
	CB	LASZIO BALOGH, Practical Considerations for MOSFET Gate Drive Techniques in High Speed, Switch-Mode Applications, Fourteenth Annual Applied Power Electronics Conference, Dallas, Texas, March 1999, APEC'99 Seminar 10	
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